PSyHCoS

User Manual

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Abstract

Real-time systems are often hard to control, due to their complicated structures, quantitative time factors and even unknown delays. We present here PSyHCoS, a tool for analyzing parametric real-time systems specified using the hierarchical modeling language PSTCSP. PSyHCoS supports several algorithms for parameter synthesis and model checking, as well as state space reduction techniques. Its architecture favors reusability in terms of syntax, semantics, and algorithms. It comes with a friendly user interface that can be used to edit, simulate and verify PSTCSP models. Experiments show its efficiency and applicability. We present here the user manual of PSyHCoS.

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Introduction

Ensuring the correctness of safety-critical systems, involving complex data structures with timing requirements, is crucial. The correctness of such real-time systems usually depends on the values of some timing delays. Checking the correctness for one particular value for each delay is usually not sufficient for two reasons. Firstly, value for the delays are not always known, and one may precisely want to *find* some values for which the system behaves well. Secondly, even if the system is proved correct for a reference set of values, one has no guarantee that the correctness holds other values around the reference one, which is also known as the *robustness* (see, e.g., [Mar11]) of the system. Hence, it is interesting to consider that the delays are unknown constants, or *parameters*, and synthesize constraints on these parameters to guarantee a correct behavior.

In this work, we present the user manual of PSyHCoS (Parameter SYnthesis for Hierarchical COncurrent Systems) [psy]. This tool performs parameter synthesis and parametric model checking for Parametric Stateful Timed CSP (PSTCSP) [ALSD12], a parametric extension of Stateful Timed CSP [SLD⁺13], itself an extension of Timed CSP (see, e.g., [Sch00]). PSTCSP offers an intuitive syntax for designing and analyzing hierarchical real-time concurrent systems involving shared variables, complex data structures, and user defined programs.

The expressiveness of PSTCSP is incomparable with Parametric Timed Automata (PTAs) [AHV93], which are an extension of finite state automata with clocks (variables increasing linearly) and parameters. Different from PTAs, clocks are *implicit* in PSTCSP, thus avoiding the designer to write manually clocks constraints, which is error-prone. Another advantage of PSTCSP over PTAs is the ability to easily define hierarchical systems. Sub-systems can be defined independently and referred in the main system. Hierarchy may allow one to handle refinement as well as closed ("black box" or "gray box") systems.

To be best of our knowledge, PSyHCoS is the first tool that synthesizes timing parameters for real-time systems handling both hierarchy and concurrency.

System Architecture

PSyHCoS offers a complete GUI for the design and the verification of PSTCSP models. We give in Figure 2.1 the architecture of PSyHCoS. Models are described using an intuitive text syntax (see Chapter 3).

PSyHCoS also features a simulator (see Figure 6.2) that can be used for step-by-step analyses, or for the graphical representation of the state space under the form of a directed graph. Beyond educational purpose, the simulator is interesting for better understanding (or debugging) models.

Among the verification algorithms, PSyHCoS first implements the inverse method IM for PSTCSP [ALSD12]. IM takes as input a PSTCSP model as well as a reference valuation for all the parameters; it synthesizes a set of parameters under the form of a convex constraint K, that guarantees the same time-abstract behavior (sequences of actions) as for the reference valuation. A major advantage is that K gives a quantitative measure of the robustness of the system w.r.t. variations of the timing delays. In particular, all linear time properties that hold for the reference valuation hold for any valuation in K. Although parameter synthesis for PSTCSP is proved undecidable (same as for PTAs), IM always terminates in practice. Also, a full reachability algorithm reachAll is implemented in PSyHCoS in order to compare optimization techniques. Other classical model checking algorithms (such as LTL, deadlock freeness, or refinement checking) are also available. Data structures and functions can be designed within PSTCSP models directly using the C# syntax.

Internal representation and optimization techniques The semantics is defined as a labeled transition system, where the states in the transition system consist of a process and a constraint on clocks and parameters [ALSD12]. In PSyHCoS, each state is implemented under the form of a pair (process id, constraint id), both under the form of a string. Although some processing is needed each time a new state is computed, the constraint equality test reduces to string equality. Also, a string format is flexible – which is necessary as we are dealing with hierarchical systems so that different states may have very different system architecture.



Figure 2.1: Architecture of PSyHCoS

PSyHCoS implements a state-space reduction technique for PSTCSP introduced in [ALSD12], which merges states that are equal except the *names* of the clocks. This may lead to an exponential diminution of the number of states, at the cost of several nontrivial operations (lists and strings sorting). The optimized version of *reachAll* (resp. IM) is denoted by *reachAll*+ (resp. IM+).

Implementation PSyHCoS is implemented in C#, based on Microsoft .NET framework. The constraint solver is based on the PPL library [BHZ08]. It can run on major OS, including Linux, Unix, Mac or Windows. The tool is available under the GNU General Public License in [psy].

Input Syntax for Models

3.1 Standard Features from PSTCSP

3.1.1 Abstract Syntax of PSTCSP

We first recall here from [ALSD12] the syntax of PSTCSP. A process models the control logic of the system using a rich set of process constructs. A process P is defined by the grammar in Figure 3.1, where $u \in U$.¹ Processes marked with * allow the use of parameters instead of timing constants in STCSP. \mathcal{P} denotes the set of all possible processes.

$P\doteq \texttt{Stop}$	inaction
Skip	termination
$ e \rightarrow P$	event prefixing
$ a\{program\} \rightarrow P$	data operation
\mid if (b) $\{P\}$ else $\{Q\}$	conditional choice
P Q	general choice
$ P \setminus E$	hiding
P;Q	sequential composition
$ P \parallel Q$	parallel composition
Wait $[u]$	delay*
P timeout[u] Q	timeout*
P interrupt $[u] $ Q	timed interrupt [*]
P within $[u]$	timed responsiveness [*]
P deadline $[u]$	$deadline^*$
$\mid Q$	process referencing

Figure 3.1: Syntax of PSTCSP processes

¹Actually, $u \in (U \cup \mathbb{Q}_{\geq 0})$ would be possible too, but having $u \in U$ simplifies the reasoning and proofs.

Definition 3.1.1 A Parametric Stateful Timed CSP (or PSTCSP) model is a tuple $M = (Var, U, V_0, P, K_0)$ where $Var \subset Var, U \subset U, V_0$ is the initial variable valuation, $P \in \mathcal{P}$ is a process, and $K_0 \in \mathcal{K}_U$ is an initial constraint.

The initial constraint K_0 allows one to define constrained models, where some parameters are already related. For example, in a timed model with two parameters *min* and *max*, one may want to constrain *min* to be always smaller or equal to *max*, i.e., $K_0 = \{min \leq max\}$.

Hierarchy comes from the nested definition of processes. Each component may have internal hierarchies, and allow abstraction and refinement, in the sense that a subprocess may be replaced by another equivalent one in some cases. Also, this offers a readable syntax, starting from the top level of the system, and being more precisely defined when one goes to lower hierarchical levels.

Untimed constructs We first briefly describe the untimed constructs, which are identical to the ones in STCSP, and very close to the ones of CSP. Process Stop does nothing but idling. Process Skip terminates, possibly after idling for some time. Process $e \to P$ engages in event e first and then behaves as P. Note that e may serve as a synchronization barrier, if combined with parallel composition. In order to seamlessly integrate data operations, sequential programs may be attached with events. Process $a\{program\} \rightarrow P$ performs data operation a (i.e., executing the sequential program whilst generating event a) and then behaves as P. The program may be a simple procedure updating data variables (e.g., $a\{v_1 := 5; v_2 := 3\}$, where $v_1, v_2 \in Var$) or a more complicated sequential program. A conditional choice is written as if (b) {P} else {Q}. If b is true, then it behaves as P; otherwise it behaves as Q. Process P|Q offers an unconditional choice² between P and Q. Process P;Q behaves as P until P terminates and then behaves as Q immediately. $P \setminus E$ hides occurrences of events in E. Parallel composition of two processes is written as $P \parallel Q$, where P and Q may communicate via multi-party event synchronization (following CSP) rules [Hoa85]) or shared variables.

Timed Constructs We now explain the parametric timed constructs.

- Given a parameter u, process Wait[u] idles for an unknown (constant) number of u time units.
- In process *P* timeout[*u*] *Q*, the *first* observable event of *P* shall occur before *u* time units elapse. Otherwise, *Q* takes over control after exactly *u* time units.
- Process P interrupt[u] Q behaves exactly as P until u time units, and then Q takes over. In contrast to P timeout[u] Q, P may engage in

 $^{^{2}}$ For simplicity, external and internal choices from the classic CSP [Hoa85] have not been defined in [ALSD12]. Nevertheless, both constructions are implemented (see Section 3.2.1), and used in our case studies.

multiple observable events before it is interrupted. Also note that Q will be executed in any case, whereas in P timeout[u] Q, process Q will only be executed if no observable event occurs before u time units.

- Process *P* within[*u*] must react within an unknown number of *u* time units, i.e., an observable event must be engaged by process *P* within *u* time units.
- Process *P* deadline[*u*] constrains *P* to terminate, possibly after engaging in multiple observable events, before *u* time units.

Discussion on deadline The deadline timed construct intuitively means that a process must terminate within a certain amount of time. Different definitions of deadline actually appear in the literature. In [FHW99], a definition of the deadline command is given, and an instantiation as an extension to the highintegrity SPARK programming language is proposed. In this case, a static analysis is performed during the compiling process and, in the case where an inability to meet the timing constraints occurs, then an appropriate error feedback is sent to the programmer. As a consequence, the deadline construction guarantees that the constrained process will terminate before the specified deadline.

In [QDC03], the authors use Unifying Theory of Programming in order to formalize the semantics of TCOZ. As in [FHW99], they consider that the deadline imposes a *timing constraint* on P, which thus requires the computation of P to be finished within the time mentioned in the deadline.

Different from [QDC03, FHW99], we here choose to stick to the semantics of STCSP [SLD⁺13] and consider a deadline semantics as an *attempt* to terminate a process before a certain time. If the process does not terminate before the deadline, it is just stopped³.

Syntactic sugar Urgent event prefixing [Dav93], written as $e \twoheadrightarrow P$, is defined as $(e \to P)$ within[0], i.e., e must occur as soon as it is enabled.

Also note that some timed constructs can be defined using other timed constructs. For instance, within can be defined using deadline.

3.1.2 Input Syntax of PSTCSP in PSyHCoS

We give in Figure 3.2 the mapping between PSTCSP syntax and the ASCIIbased PSyHCoS syntax. Since some parts of PSyHCoS rely on the PAT model checker [SLDP09, LSD11], PSyHCoS's input syntax is rather similar to the STCSP input syntax in the RTS module of PAT.

³Remark that, in that case, time elapsing may be stopped too.

```
Stop
                          Stop
Skip
                          Skip
e \to P
                          e -> P
a\{program\} \rightarrow P
                          a { program } -> P
if (b) \{P\} else \{Q\} if b \{P\} else \{Q\}
P|Q
                          P [] Q
P \setminus E
                          P \setminus \{ e1, e2, e3 \}
P;Q
                          P;Q
P \parallel Q
                          P || Q
\mathtt{Wait}[u]
                          Wait[u]
P \operatorname{timeout}[u] Q
                          P timeout[u] Q
P \text{ interrupt}[u] Q
                          P interrupt[u] Q
                          P within[u]
P \text{ within}[u]
P \text{ deadline}[u]
                          P deadline[u]
P = Q
                          P = Q
```

Figure 3.2: Syntax of PSTCSP processes in PSyHCoS

3.2 Syntactic Extensions of PSTCSP

3.2.1 Additional Operations

We give in Figure 3.3 additional operations on PSTCSP not strictly defined in [ALSD12], but implemented in PSyHCoS.

P [*] Q	External choice
P <> Q	Internal choice
if (cond1) $\{P\}$ else if (cond2) $\{Q\}$ else $\{M\}$	Conditional choice (nested)
ifa (cond) {P} else {Q}	Atomic conditional choice
ifb (cond) {P}	Blocking conditional choice
[cond] P	Guarded process
P Q	Interleaving
atomic { P }	Atomicity

Figure 3.3: Syntax of advanced PSTCSP processes in PSyHCoS

3.2.2 Advanced Constructs

Constants Constants can be defined as follows: #define max 5;

Variables Variables can be defined as follows: var knight = 0; The value is optional. Example: var knight;

Parameters Parameters can be defined as follows: **parameter Delta**; Recall that parameters are unknown constants. As a consequence, they cannot be set, nor updated.

Data structures PSyHCoS supports advanced data structures such as lists, arrays, matrices, and user-defined structures. A fixed-size array may be defined as follows:

var board = [3, 5, 6, 0, 2, 7, 8, 4, 1]; where board is the array name and its initial value is specified as the sequence, e.g., board[0] = 3. The following defines an array of size 3.

var leader[3];

All elements in the array are initialized to be 0.

Multi-dimensional arrays may also be defined as follows.

var matrix[3*N][10];

Note: To assign values to specific elements in an array, you can use event prefix. For example:

P() = a { matrix[1][9] = 0 } -> Skip;

To ease the modeling, PSyHCoS allows users to define any data structures and use them in PSTCSP models. The following shows the syntax.

var<Type> x ; //default constructor of Type class will be called. var<Type> x = new Type(1, 2); //constructor with two int parameters will be called.

3.3 Example: Fischer Mutual Exclusion

We use here an example from [ALSD12] in order to present PSyHCoS's syntax, and show PSTCSP's intuitive modeling facilities. Fischer's mutual exclusion algorithm is modeled by a process FME making use of a set of timing parameters $U = \{\delta, \gamma\}$, and a set of variables $Var = \{turn, cnt\}$. The turn variable indicates which process attempted to access the critical section most recently. The cnt variable counts the number of processes accessing the critical section. Initial valuation maps turn to -1 (which denotes that no process is attempting initially) and cnt to 0 (which denotes that no process is in the critical section initially). Process FME is defined as follows⁴.

 $\begin{array}{ll} FME &\doteq proc(1) \parallel proc(2) \parallel \cdots \parallel proc(n) \\ proc(i) &\doteq \texttt{if} \ (turn = -1) \ \{Active(i)\} \texttt{else} \ \{proc(i)\} \\ Active(i) &\doteq (update.i\{turn := i\} \rightarrow \texttt{Wait}[\gamma]) \ \texttt{within}[\delta]; \\ \texttt{if} \ (turn = i) \\ cs.i\{cnt := cnt + 1\} \rightarrow \\ exit.i\{cnt := cnt - 1; turn := -1\} \rightarrow proc(i) \\ \texttt{else} \\ proc(i) \end{array}$

 $^{^4\}mathrm{Note}$ that this is not the real ASCII-based PSyHCoS syntax.

where n is a constant representing the number of processes. Process proc(i) models a process with a unique integer identify i. If turn is -1 (i.e., no other process is attempting), proc(i) behaves as specified by process Active(i). In process Active(i), turn is first set to i (i.e., the *i*th process is now attempting) by action update.i. Note that update.i must occur within δ time units (captured by within[δ]). Next, the process idles for γ time units (captured by Wait[γ]). It then checks if turn is still i. If so, it enters the critical section and leaves later. Otherwise, it restarts from the beginning.

We give below the PSyHCoS-based syntax for *FME*.

```
#define N 3;
1
 \mathbf{2}
    #define Idle −1;
 3
 4
    var turn = Idle;
    var counter = 0;
\mathbf{5}
 6
    parameter Delta;
 7
    parameter Epsilon;
 8
    proc(i) = ifb(turn == Idle) { Active(i) };
9
10
    Active(i) = ((update.i{turn=i} -> Wait[Epsilon]) within [Delta]);
11
       if (turn == i) {
12
          cs.i{counter++} -> exit.i{counter--; turn=Idle}->proc(i)
13
       }
         else
14
          proc(i)
15
       };
16
   FME = ||| i : \{0..N-1\}@proc(i);
17
```

Input Syntax for Analysis

4.1 Parameter Synthesis Using the Inverse Method

PSyHCoS implements the inverse method IM for PSTCSP [ALSD12], initially defined for PTAs [ACEF09, AS13]. IM takes as input a PSTCSP model as well as a reference valuation for all the parameters; it synthesizes a set of parameters under the form of a convex constraint K, that guarantees the same time-abstract behavior (sequences of actions) as for the reference valuation. A major advantage is that K gives a quantitative measure of the robustness of the system w.r.t. variations of the timing delays. In particular, all linear time properties that hold for the reference valuation hold for any valuation in K. Although parameter synthesis for PSTCSP is proved undecidable (same as for PTAs), IM always terminates in practice.

The input syntax is of the following form:

#synthesize process with u1 = 2, u2 = 4;

1

4.2 Full Reachability Analysis

A full reachability algorithm *reachAll* is implemented in PSyHCoS in order to compare optimization techniques (see experiments in [ALSD12]). It computes the whole set of reachable states; note that this algorithm may not always terminate.

The input syntax is of the following form:

1 #synthesize process reachesall;

Returned statistics include the number of states and transitions, the memory occupation and the computation time. All states can also be returned in a text form (for a graphical form, use the simulator).

4.3 Model Checking

Various model checking algorithms are implemented within PSyHCoS. An example of syntax (for deadlock freeness checking) is given below:

```
1 #assert P deadlockfree;
```

1

4.4 Example: Fischer Mutual Exclusion

Recall example from Section 3.3. A classical parameter synthesis problem is to find values for δ and γ such that mutual exclusion is guaranteed. This is achieved by the following call to the inverse method:

#synthesize FME with Epsilon = 3, Delta = 4;

The constraint synthesized by PSyHCoS for Fischer is $\delta < \gamma$, viz., the weakest constraint known to guarantee mutual exclusion.

4.5 State Space Optimization

As explained in [ALSD12], some states in PSTCSP considered as different are actually equivalent. Consider the following two states:

$$\begin{split} s_1 &= (\emptyset, \texttt{Wait}[u_1]_{x_1}\texttt{deadline}[u_2]_{x_2}, x_1 \leq x_2 \leq u_2) \\ s_2 &= (\emptyset, \texttt{Wait}[u_1]_{x_2}\texttt{deadline}[u_2]_{x_1}, x_2 \leq x_1 \leq u_2) \end{split}$$

It is obvious that $s_1 = s_2$, except the *names* of the clocks. Merging these states may lead to an exponential diminution of the number of states.

Hence, we implemented a technique of *state normalization*: First, the clocks in the process are renamed so that the first one (from left to right) is named x_1 , the second x_2 , and so on. Second, the variables in the constraint are swapped accordingly. This technique solves this problem at the cost of several nontrivial operations (lists and strings sorting). We denote by *reachAll*+ (resp. IM+) the version of *reachAll* (resp. IM) using this technique.

This technique can be used for most algorithm; the choice is available in the verification window.

A Complete Example

We give below an entire example of input model for PSyHCoS, with commands for verification and parameter synthesis.

A full list of benchmarks is available in [psy].

```
1
     parameter pDelta;
 \mathbf{2}
    parameter pEpsilon;
 3
    #define N 3;
 4
    #define Delta 3;
 5
 6
    #define Epsilon 4;
 7
    #define Idle -1;
 8
 9
10
    var x = Idle;
11
    var counter;
12
13
14
    //untimed version
    uP(i) = ifb(x == Idle) \{
15
16
              update.i \{x = i\} \rightarrow
               if (x == i) {
17
18
                  cs.i{counter++} -> exit.i{counter--; x=Idle} -> uP(i)
19
                 else {
               }
20
                    uP(i)
21
                }
22
           };
    uFischersProtocol = ||| i: {0..N-1}@uP(i);
23
24
    #synthesize uFischersProtocol reachesall;
25
26
27
    //timed version
    P(i) = ifb(x = Idle) {
28
29
           ((update.i \{x = i\} \rightarrow Wait [Epsilon]) within [Delta]);
30
              if (x = i) \{
                  cs.i{counter++} \rightarrow exit.i{counter--; x=Idle} \rightarrow P(i)
31
32
                else {
              }
33
                    P(i)
34
              }
35
           };
```

```
36
37
   FischersProtocol = ||| i: \{0..N-1\}@P(i);
38
   #synthesize FischersProtocol reachesall;
39
40
   // parametric timed version
PP(i) = ifb(x == Idle) {
41
42
          ((update.i{x = i}) \rightarrow Wait[pEpsilon]) within[pDelta]);
43
44
              if (x = i) {
45
                 cs.i{counter++} -> exit.i{counter--; x=Idle} -> PP(i)
              } else {
PP(i)
46
47
48
              }
49
           };
50
    FischersProtocolParam = ||| i: {0..N-1}@PP(i);
51
52
    #synthesize FischersProtocolParam with
53
54
       pEpsilon = 3,
55
       pDelta = 4
56
   #synthesize FischersProtocolParam reachesall;
57
```

Screenshots

🗟 Fischer.par	ExRTSS.par	Assertio			- Fischer.par	
43	((update.i{x = i} -> Wait[pEps:	il 🕐 1	uFischersProtocol() rea	chesall		
44	if (x == i) {	2	2 FischersProtocol() reachesall			
45	cs.i{counter++} -> exit	t. 🕥 3				
46) else (? 4	FischersProtocolParam			
47	PP (1)			0		
48 -	}					
49);					
50						
	rsProtocolParam = i:{0N-1}6	a P				
52		<				
	esize FischersProtocolParam with	Selecte	Selected Assertion			
	psilon = 3,					
	elta = 4					
56			Verify	View Büchi Autor	mata 🔰 Simulate Wil	ness Trace
57 #swnth	esize FischersProtocolParam reach	ne or				
<∥		Options				
		Admis	sible Behavior	 Timed 	d out after (minutes)	120 😜
			ation Engine			_
Output Window				 Liene 	rate Witness Trace	
Output Window						
2 🖬	1 0,0085091s	Output				
2 🛃 pecification is parsed	n 0,0085091s =======Global Constants==========					
2 🛃 pecification is parsed		=- Output	an assertion to start v			

Figure 6.1: Editor and verification engine



Figure 6.2: Simulation interface

Authors and License

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The tool, the underlying theory and this user manual have been made by the following authors:

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